IN THE CLAIMS

Please amend claims 1-6, 8, 10-15, 17 and 19-23 as follows.

1. (Amended) A termination resistor circuit, provided in an interface circuit through which signals are transferred, comprising:

a first termination resistor block having a plurality of transistors with a same logic voltage being applied to gates of the transistors of said first termination resistor block ; and

a second termination resistor block having a plurality of transistors with different logic voltages being applied to gates of the transistors of said second termination resistor block, which differs in configuration from said first termination resistor block, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

2. (Amended) The termination resistor circuit as claimed in claim 1, wherein:

said first termination resistor block comprises transistors of a same conductivity type; and

said second termination resistor block comprises transistors of different conductivity types.

3. (Amended) A termination resistor circuit provided in an interface circuit through which signals are transferred via a transmission line, comprising:

a first termination resistor block having a plurality of transistors, a gate of at least one of the transistors of said first termination resistor block being applied with a supply voltage or a voltage of said transmission line; and

a second termination resistor block having a plurality of transistors, and which differs in configuration from said first termination resistor block, and wherein,

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

4. (Amended) The termination resistor circuit as claimed in claim 3, wherein:

said first termination resistor block comprises transistors of a same conductivity type; and

said second termination resistor block comprises transistors of different conductivity types.

5. (Amended) A termination resistor circuit provided in an interface circuit through which signals are transferred via a transmission line comprising:

a first termination resistor block having a plurality of transistors of a same conductivity type; and

second termination resistor block, which differs in configuration from said first termination resistor block, wherein

said first termination resistor block operates and maintains a specific resistance value when a signal of said transmission line is near a supply voltage; and

said second termination resistor block comprises a first conductivity type transistor which does not operate near a first supply voltage, and a second conductivity type transistor which does not operate near a second supply voltage, and

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

Cont'd A1

6. (Amended) The termination resistor circuit as claimed in claim 4, wherein said first and second transistors are chosen to have a size for each of said first termination resistor blocks so that said plurality of first termination resistor blocks have respectively chosen weights.

Sub B4 A2

8. The termination resistor circuit as claimed in claim 4, wherein said third and fourth transistors are chosen to have a size for each of said second termination resistor blocks so that said plurality of second termination resistor blocks have respectively chosen weights.

Sub B6 A3

10. (Amended) A signal transmission system comprising:

a transmitting circuit for transmitting a signal;

a transmission line for transmitting the signal output from said transmitting circuit;

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a plurality of transistors with a same logic

voltage being applied to gates of the transistors of said first termination resistor block ; and

a second termination resistor block having a plurality of transistors with different logic voltages being applied to gates of the transistors of said second termination resistor block, which differs in configuration from said first termination resistor block, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

11. (Amended) The signal transmission system termination as claimed in claim 10, wherein:

said first termination resistor block comprises transistors of a same conductivity type; and

said second termination resistor block comprises transistors of different conductivity types.

12. (Amended) A signal transmission system comprising:

a transmitting circuit for transmitting out a signal;

a transmission line for transmitting therethrough the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in

an interface circuit through which signals are transferred, wherein said termination resistor comprises:

a first termination resistor block having a plurality of transistors, a gate of at least one of the transistors of said first termination resistor block being applied with a supply voltage or a voltage of said transmission line; and

a second termination resistor block having a plurality of transistors, which differs in configuration from said first termination resistor block, and wherein,

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

13. (Amended) The signal transmission system as claimed in claim 12, wherein,

said first termination resistor block comprises transistors of a same conductivity type; and

said second termination resistor block comprises transistors of different conductivity types.

14. (Amended) A signal transmission system comprising:

a transmitting circuit for transmitting out a signal;

a transmission line for transmitting therethrough the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a plurality of transistors of a same conductivity type; and

a second termination resistor block, which differs in configuration from said first termination resistor block, and wherein:

said first termination resistor block operates and maintains a specific resistance value when a signal of said transmission line is near a supply voltage; and

said second termination resistor block comprises a first conductivity type transistor which does not operate near a first supply voltage, and a second conductivity type transistor which does not operate near a second supply voltage, and wherein,

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

15. The signal transmission system as claimed in claim 13, wherein said first and second transistors are chosen to have a size for each of said first termination resistor blocks so that said plurality of first termination resistor blocks have respectively chosen weights.

17. The signal transmission system as claimed in claim 13, wherein said third and fourth transistors are chosen to have a size for each of said second termination resistor blocks so that said plurality of second termination resistor blocks have

respectively chosen weights.

19. (Amended) A signal transmission system comprising:

a transmission line for transmitting a signal;

a receiving circuit for receiving the signal transmitted through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a plurality of transistors with a same logic voltage being applied to gates of the transistors of said first termination resistor block; and

a second termination resistor block having a plurality of transistors with different logic voltages being applied to gates of the transistors of said second termination resistor block, which differs in configuration from said first termination resistor block, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

20. (Amended) The signal transmission system termination as claimed in claim 19, wherein:

said first termination resistor block comprises transistors of a same conductivity type; and

said second termination resistor block comprises transistors of different conductivity types.

21. (Amended) A signal transmission system comprising:

a transmission line for transmitting a signal;

a receiving circuit for receiving the signal transmitted through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a plurality of transistors, a gate of at least one of the transistors of said first termination resistor block being applied with a supply voltage or a voltage of said transmission line; and

a second termination resistor block having a plurality of transistors, which differs in configuration from said first termination resistor block, and wherein,

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

22. (Amended) The signal transmission system as claimed in claim 21, wherein,

said first termination resistor block comprises transistors of a same conductivity type; and

said second termination resistor block comprises transistors of different

conductivity types.

23. (Amended) A signal transmission system comprising:

a transmitting circuit for transmitting out a signal;

a transmission line for transmitting therethrough the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a plurality of transistors, the same logic voltage being applied to gates of the transistors of said first termination resistor block; and

a second termination resistor block having a plurality of transistors with different logic voltages being applied to gates of the transistors of said second termination resistor block, which differs in configuration from said first termination resistor block, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.